

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named

Applicant: Steve K. Hsia

Docket No.: 11030.02

Title: METHOD AND APPARATUS FOR MULTIPLE BYTE OR PAGE MODE  
PROGRAMMING AND READING OF A FLASH MEMORY ARRAY

Serial No.

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Name: Jane Lalis

Commissioner for Patents  
Washington, D.C. 20231


FILING OF INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)

This Information Disclosure Statement is being filed concurrently with the above-referenced patent application. In accordance with 37 CFR § 1.97(b) to the present knowledge of the undersigned, the accompanying Information Disclosure Citation and the information contained herein are being filed within any one of the following time periods: within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR § 1.491 in an international application; before the mailing of a first Office action on the merits; or before the mailing of a first Office action after the filing of a request for continued examination under 37 CFR § 1.114. Pursuant to 37 CFR § 1.97(b), no fee is required.

The filing of this information disclosure statement, including citation of the listed documents, shall not be construed as an admission that the information furnished is, or is considered to be, material to patentability as defined in, for example, section 1.56(b), or prior art with respect to the present invention, or as a representation that that no other material information exists or that search has been made (other than specifically described above). The filing of this information disclosure statement shall not be construed as an admission against interest in any manner.

Respectfully submitted,

Date: Nov. 8, 2001



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